

SEGGER's Advanced Debug Technology Made Available for STM32 Nucleo & Discovery MCU Boards

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SEGGER has now introduced J-Link firmware for the embedded ST-LINK on STM32 Nucleo, STM32 Discovery and other microcontroller unit (MCU) evaluation boards from STMicroelectronics. This brings the proven reliability and industry-leading performance of SEGGER's highly popular J-Link debug technology to these items of hardware, thereby leading to more efficient development processes.



The upgrade turns a board's ST-LINK interface into a fully functional J-Link, which can then be employed to debug the board's application processor. By upgrading to the J-Link firmware, developers will subsequently be able to set an unlimited number of breakpoints in Flash memory and debug their applications considerably faster - resulting in better use of engineering resources and shortened time to market.

The J-Link firmware offers compatibility with all major tool chains, including those using GDB (GNU Debugger), on all major host platforms (Windows, Mac, Linux), while also attaining elevated operational benchmarks, including the fastest download speed into RAM and Flash memory. The tool needed to upgrade the firmware of the on-board ST-LINKs can be downloaded free of charge from SEGGER's website. If it ever becomes necessary to do so, the exchange process is completely reversible, restoring the original ST-LINK functionality.

"With J-Link firmware installed, users of STM32 Nucleo and STM32 Discovery evaluation boards, amongst others, will benefit from access to essential tools like SEGGER RTT and SystemView for real-time analysis purposes. In doing so, they will get the opportunity to experience the unmatched capabilities that our J-Link technology offers," states Alex Grüner, CTO of SEGGER.

"The availability of J-Link firmware for ST-LINK provides a professional debug solution across the complete range of ST evaluation boards at all price points, from low-cost to feature-rich. It showcases the high flash programming speed of the ST boards and enables even more tool chains to use our STM32 Nucleo and STM32 Discovery boards. Making J-Link available for these boards demonstrates the strength of the STM32 family as we continue to expand the ecosystem and further accelerate design and debug for our customers," adds Daniel Colonna, Marketing Director, Microcontroller Division, STMicroelectronics.

To access more information on converting from ST-LINK go to: <https://www.segger.com/jlink-st-link.html>

Further information on J-Link is available at: <http://www.segger.com/jlink.html>



About J-Link

The SEGGER J-Link is the most popular family of debug probes on the market. It is tool chain independent and works with free GDB-based tool chains such as emIDE and Eclipse, as well as commercial IDEs from: Atmel, Atollic, Coocox, Cosmic, Freescale, IAR, KEIL, Mentor Graphics, Microchip, Python, Rowley, Renesas, Tasking and others. With the J-Link family, investments in the debug probe are preserved when changing compiler or even CPU architecture.

J-Link supports multiple CPU families, such as ARM 7, 9, 11, Cortex-M, Cortex-R, Cortex-A as well as Renesas RX100, RX200, RX600 and Microchip PIC32; there is no need to buy a new J-Link or new license when switching to a different yet supported CPU family or tool-chain. SEGGER is also continuously adding support for additional cores, which in most cases, only requires a software/firmware update. Unlimited free updates are included with even the baseline model of the J-Link family. SEGGER is excited to continue advanced development of its cutting edge embedded tool solutions to be utilized with pretty much any development environment you choose. All J-Links are fully compatible to each other, so an upgrade from a lower-end model to a higher-end model is a matter of a simple plug-and-play.

Full product specifications are available at: www.segger.com/jlink.html

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About SEGGER

SEGGER Microcontroller develops and distributes hardware and software development tools as well as software components for embedded systems. An "embedded system" is one in which a microprocessor and associated components are incorporated into a device helping to accomplish difficult and complex tasks in products such as cell phones, medical instruments, instrument clusters, measurement instruments, satellite radios, digital cameras etc.

SEGGER was founded in 1997, is privately held, and is growing steadily. Based in Hilden with distributors in all continents and a local office in Massachusetts, SEGGER offers its full product range worldwide.

SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB host and device stack) and embOS/IP (TCP/IP stack). With emSecure, a unique software to generate and verify digital signatures, and the TLS-solution emSSL, SEGGER is also offering software for the growing field of data and product security.

With the experience in programming efficiently on embedded systems, SEGGER created highly integrated, cost-effective programming and development tools, such as the Flasher (stand-alone flash programmer) and the industry leading J-Link/J-Trace emulator.

SEGGER cuts software development time for embedded applications by offering affordable, high quality, flexible and easy-to-use tools and software components allowing developers to focus on their applications. Find out more at www.segger.com

Contact information:

Dirk Akemann
Marketing Manager
Tel: +49-2103-2878-0



E-mail: info@segger.com

Issued on behalf of:

SEGGER Microcontroller GmbH & Co. KG
In den Weiden 11
40721 Hilden
Germany
www.segger.com

SEGGER Microcontroller Systems LLC
106 Front Street
Winchendon, MA 01475
United States of America
www.segger-us.com

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