

Program any SPI Flash via J-Link and Flasher – regardless of CPU model

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The newly released J-Flash SPI software allows SEGGER's in-circuit programmers and debug probes to support direct programming of SPI flashes.

Users of current models of J-Link PLUS, ULTRA+ and PRO, as well as Flasher ARM and PRO can employ J-Flash SPI to program serial (SPI) flashes, which are directly connected to the J-Link or Flasher.

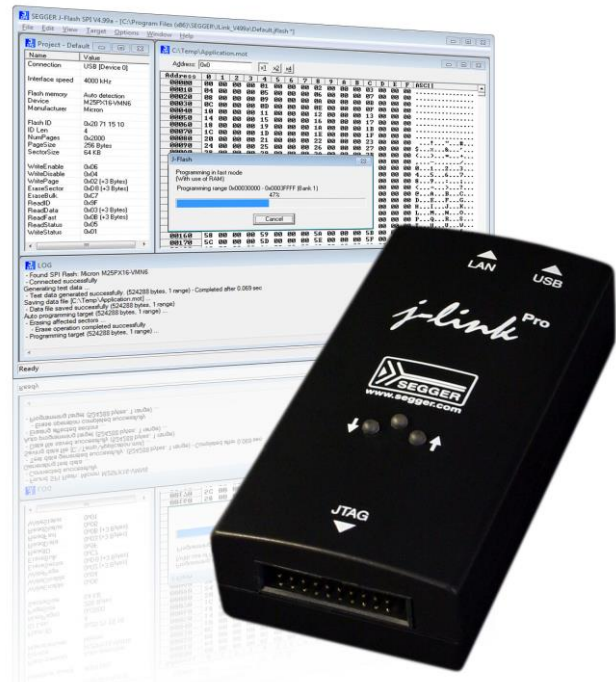
"Serial flashes have become increasingly popular, also in typical microcontroller applications. Especially SPIFI interfaces, which allow treating SPI flashes as memory mapped, have contributed to this popularity," says Dirk Akemann, Partnership Marketing Manager at SEGGER. "The ability to program these flashes with J-Link and Flasher adds a lot of value to these products."

Typically, the CPU of the target system is held in reset so that J-Link has full access to the flash. It can read back, verify, program and erase all common SPI flashes from various manufacturers.

As talking directly to the flash, this way of programming also allows to program an SPI flash which is connected to a CPU model currently not directly supported by J-Link or Flasher.

J-Flash SPI is part of the latest version of the J-Link software and documentation pack.

More information on J-Flash SPI is available at: www.segger.com/jflash-spi.html



About J-Link

The SEGGER J-Link is the most popular family of debug probes on the market. It is tool chain independent and works with free GDB-based tool chains such as emIDE and Eclipse, as well as commercial IDEs from: Atmel, Atollic, Coocox, Cosmic, Freescale, IAR, KEIL, Mentor Graphics, Microchip, Python, Rowley, Renesas, Tasking and others. With the J-Link family, investments in the debug probe are preserved when changing compiler or even CPU architecture.

J-Link supports multiple CPU families, such as ARM 7, 9, 11, Cortex-M, Cortex-R, Cortex-A as well as Renesas RX100, RX200, RX600 and Microchip PIC32; there is no need to buy a new J-Link or new license when switching to a different yet supported CPU family or tool-chain. SEGGER is also continuously adding support for additional cores, which in most cases, only requires a software/firmware update. Unlimited free updates are included with even the baseline model of the J-Link family. SEGGER is excited to continue advanced development of its cutting edge embedded tool solutions to be utilized with pretty much any development environment you choose. All J-Links are fully compatible to each other, so an upgrade from a lower-end model to a higher-end model is a matter of a simple plug-and-play.

Full product specifications are available at: www.segger.com/jlink.html

The J-Link-Software is available at: www.segger.com/download_jlink.html

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About SEGGER

SEGGER Microcontroller develops and distributes hardware and software development tools as well as software components for embedded systems. An "embedded system" is one in which a microprocessor and associated components are incorporated into a device helping to accomplish difficult and complex tasks in products such as cell phones, medical instruments, instrument clusters, measurement instruments, satellite radios, digital cameras etc.

SEGGER was founded in 1997, is privately held, and is growing steadily. Based in Hilden with distributors in all continents and a local office in Massachusetts, SEGGER offers its full product range worldwide.

SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB host and device stack) and embOS/IP (TCP/IP stack). With the experience in programming efficiently on embedded systems, SEGGER created highly integrated, cost-effective programming and development tools, such as the Flasher (stand-alone flash programmer) and the industry leading J-Link/J-Trace emulator.

SEGGER cuts software development time for embedded applications by offering affordable, high quality, flexible and easy-to-use tools and software components allowing developers to focus on their applications. Find out more at www.segger.com.

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