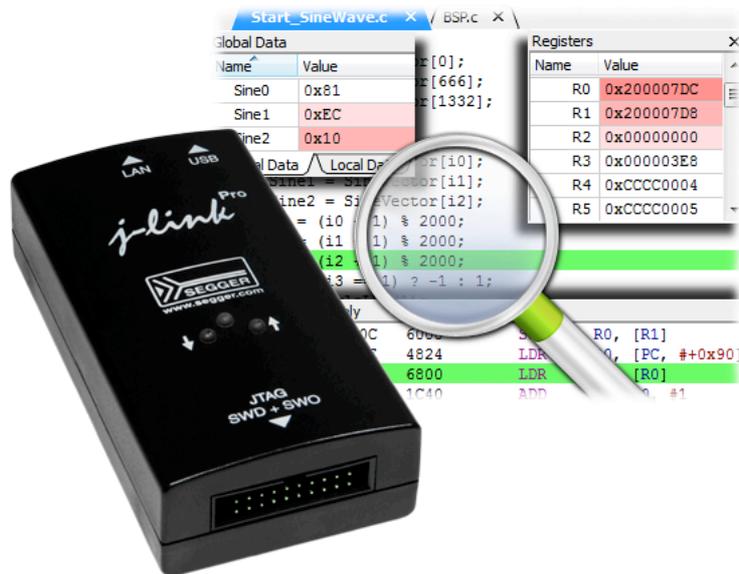


## Powerful J-Link Debugger software now included with advanced J-Link models

Hilden, Germany – May 12<sup>th</sup>, 2015

SEGGER has released its J-Link Debugger software for embedded applications. As expected, the same high level of design and engineering found in the J-Link debug probes is found in the J-Link Debugger software.

In addition to standard debugging features like C source and assembly level debugging, J-Link Debugger also supports high-end debugging techniques such as SEGGER's Real Time Terminal (RTT) for unique, bidirectional, high-speed Terminal I/O (printf), setting an unlimited number of breakpoints even when debugging in flash memory, instruction trace, and ultra-fast flash download with J-Link/J-Trace. In addition to SEGGER RTT, other basic terminal I/O techniques like ARM's Serial Wire Output (SWO) and semihosting are also supported.



J-Link Debugger can load applications/ELF files built with any compiler or even debug the target's resident application without any source files being available. It is also fully controllable via script files making it the perfect solution for automated test setups. Instruction trace is also available in J-Link Debugger, allowing to view the stack of most recently executed machine instructions. This allows a better means of debugging complex problems and bugs.

The J-Link Debugger has been designed specifically for the advanced set of SEGGER's industry-leading J-Link debug probes. This includes the J-Trace, J-Link PRO, J-Link ULTRA+, and J-Link PLUS. Other J-Link models may be used with the evaluation version of the J-Link Debugger.

More information on J-Link is available at: <https://segger.com/j-link-debugger.html>

### About J-Link

The SEGGER J-Link is the most popular family of debug probes on the market. It is tool chain independent and works with free GDB-based tool chains such as emIDE and Eclipse, as well as commercial IDEs from: Atmel, Atollic, Coocox, Cosmic, Freescale, IAR, KEIL, Mentor Graphics, Microchip, Python, Rowley, Renesas, Tasking and others. With the J-Link family, investments in the debug probe are preserved when changing compiler or even CPU architecture.

J-Link supports multiple CPU families, such as ARM 7, 9, 11, Cortex-M, Cortex-R, Cortex-A as well as Renesas RX100, RX200, RX600 and Microchip PIC32; there is no need to buy a new J-Link or new license when switching to a different yet supported CPU family or tool-chain. SEGGER is also continuously adding support for additional cores, which in most cases, only requires a software/firmware update. Unlimited free updates are included with even the baseline model of the J-Link family. SEGGER is excited to continue advanced development of its cutting edge embedded tool solutions to be utilized with pretty much any development environment you choose. All J-Links are fully compatible to each other, so an upgrade from a lower-end model to a higher-end model is a matter of a simple plug-and-play.

Full product specifications are available at: <http://segger.com/jlink.html>

The J-Link-Software is available at: [http://segger.com/download\\_jlink.html](http://segger.com/download_jlink.html)



U.S. On-Line Web Shop: <http://shop-us.segger.com>

Online Shop (Europe, Asia, Africa): <http://shop.segger.com>

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## About SEGGER

**SEGGER Microcontroller** develops and distributes hardware and software development tools as well as software components for embedded systems. An "embedded system" is one in which a microprocessor and associated components are incorporated into a device helping to accomplish difficult and complex tasks in products such as cell phones, medical instruments, instrument clusters, measurement instruments, satellite radios, digital cameras etc.

SEGGER was founded in 1997, is privately held, and is growing steadily. Based in Hilden with distributors in all continents and a local office in Massachusetts, SEGGER offers its full product range worldwide.

SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB host and device stack) and embOS/IP (TCP/IP stack). With the experience in programming efficiently on embedded systems, SEGGER created highly integrated, cost-effective programming and development tools, such as the Flasher (stand-alone flash programmer) and the industry leading J-Link/J-Trace emulator.

SEGGER cuts software development time for embedded applications by offering affordable, high quality, flexible and easy-to-use tools and software components allowing developers to focus on their applications. Find out more at <http://www.segger.com>.

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