J-Link Flash Download Technology breaks Speed Record

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SEGGER's J-Link achieves a new speed record in programming flash memory. The new technology achieves a performance very close to the maximum speed possible while fully maintaining the highest standards in reliability. The SEGGER flash loaders include a verification of each block written and final checksum verification to guarantee proper operation.

This record has been achieved on an STM32F4 device, where J-Link programs 256 Kbytes/s, making it possible to program a 1MB device in 4 Seconds.

No other debug probe comes close to the performance of the J-Link. In a comparison test; the closest competitor took 4 times as long to program the same device.

The same technology works for all supported Cortex-M microcontrollers, for internal and external flash. The exact speed achieved depends on the device.

It is very easy to take advantage of this great increase in speed when working with any of the supported IDEs. Simply using the latest SEGGER J-Link software will supercharge the IDE. The software is available free of charge for existing J-Link users. On some IDEs it is necessary to upgrade to the fast SEGGER J-Link flash loader rather than using the basic flash loader provided by the IDE vendor.

While debugging in flash memory of target hardware, the key variable in debugging speed is not the IDE but rather, the performance of the flash loaders and debug probe.

"A high flash download speed is very important both in production and during development. It demonstrates once again the leading position SEGGER has in this field. SEGGER's flash programming algorithm outperforms the competition by far, including much more expensive probes", says Alexander Gruener, Product Manager for J-Link.

More details on the performance comparison can be found at:
http://www.segger.com/jlink-flash-download.html

About J-Link

The SEGGER J-Link is the industry-standard for ARM debug emulators, supported by all major tool chains for ARM cores. The SEGGER J-Link is tool chain independent and will work with freely available and commercial IDEs from: Atmel, Atollic, Coocox, Freescale, IAR, i-Systems, ImageCraft, KEIL, Mentor Graphics, Phyton, Rowley, Renesas, Tasking, and others. In addition to those listed above; any RDI compliant debugger can be used with the optional RDI module, and any GDB compliant debugger with the free GDB-Server. Therefore; as projects change, a different compiler/debugger may have to be used. With the J-Link family, investments (monetary and learning curve) in development/ production tools are preserved. Setup of a J-Link is done in mere minutes.

J-Link supports multiple CPU families, such as ARM 7, 9, 11, Cortex-M0, M1, M3, M4, R4, A5, A8, A9, Renesas RX in a single model; there is typically no need to buy a new J-Link or new license when switching to a different CPU family or tool-chain. SEGGER is also continuously adding support for additional cores, which in most cases, only requires a software/firmware update. Unlimited free updates are included with even the baseline model of the J-Link. SEGGER is excited to continue advanced development of its cutting edge embedded tool solutions to be utilized with pretty much any development environment you choose. All J-Links are fully compatible to each other, so an upgrade from a lower-end model to a higher end model is a matter of a simple plug-and-play.

Full product specifications are available at: http://www.segger.com/jlink.html

The J-Link-Software is available at: http://www.segger.com/download_jlink.html

U.S. On-Line Web Shop: http://shop-us.segger.com

Online Shop (Europe, Asia, Africa): http://shop.segger.com

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About SEGGER

SEGGER Microcontroller develops and distributes hardware and software development tools as well as software components for embedded systems. An "embedded system" is one in which a microprocessor and associated components are incorporated into a device helping to accomplish difficult and complex tasks in products such as cell phones, medical instruments, instrument clusters, measurement instruments, satellite radios, digital cameras etc.

SEGGER was founded in 1997, is privately held, and is growing steadily. Based in Hilden with distributors in all continents and a local office in Massachusetts, SEGGER offers its full product range worldwide.

SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB host and device stack) and embOS/IP (TCP/IP stack). With the experience in programming efficiently on embedded systems, SEGGER created highly integrated, cost-effective programming and development tools, such as the Flasher (stand-alone flash programmer) and the industry leading J-Link/J-Trace emulator.

SEGGER's intention is to cut software development time for embedded applications by offering affordable, high quality, flexible and easy-to-use tools and software components allowing developers to focus on their applications. Find out more at http://www.segger.com.

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