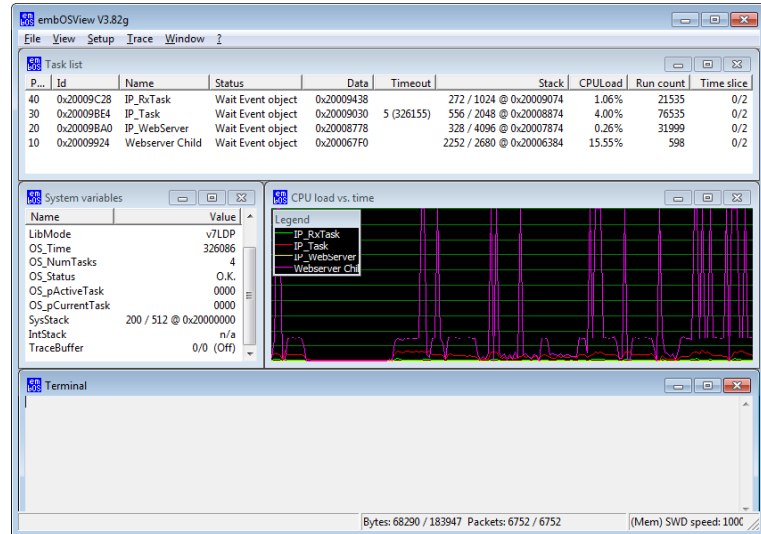


SEGGER's embOSView uses J-Link debug interface for Cortex-M-targets

Hilden, Germany – July 6th, 2010 – SEGGER Microcontroller published a new version of embOS and the included profiling tool embOSView for the ARM Cortex-M0/M1/M3/M4 platforms. The kernel information and profiling data for embOSView can now be captured through the J-Link via JTAG or SWD similar to the solution via DCC for ARM7/9-targets. embOSView not only uses the same debug interface as the debugger, but it also can run in parallel.

In contrast to SWO or UART based solutions, embOSView can be used with any Cortex-M-hardware using the existing debug connection. Therefore the target hardware can be designed with a minimal pin count for the debug connection.

Compared with SWO based kernel view solutions, using the JTAG/SWD-connection has the benefit of a bi-directional connection. Therefore embOSView only communicates with the target, if absolutely necessary.



embOSView is delivered as part of embOS, the highly efficient, easy to use RTOS from SEGGER. embOS is a high-performance real time OS that has been optimized for minimum memory consumption in both RAM and ROM, as well as high speed and versatility. It supports fully nested interrupts for zero interrupt latency and an unlimited number of tasks without the need for compile-time configuration. embOS is a priority-controlled multi-tasking system, designed as an embedded operating system for real-time applications for all popular CPUs. It provides a migration path with identical APIs across all platforms.

"We constantly strive to improve our products. Allowing the use of embOSView via the JTAG/SWD-connection gives our customers the possibility to create a more cost effective hardware without sacrificing debugging options", says Dirk Akemann, Marketing Manager at SEGGER.

embOS is provided as full source code and comes with a simple licensing model without royalties. Full product specifications and a trial version are available at: <http://www.segger.com/>

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About SEGGER

SEGGER Microcontroller develops and distributes hardware and software development tools as well as software components for embedded systems. An "embedded system" is one in which a microprocessor and associated components are incorporated into a device helping to accomplish difficult and complex tasks in products such as cell phones, medical instruments, instrument clusters, measurement instruments, satellite radios, digital cameras etc.

SEGGER was founded in 1997, is privately held, has been profitable since its inception, and is growing steadily. Based in Hilden with distributors in all continents and a local office in Massachusetts, SEGGER offers its full product range worldwide.

SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB host and device stack) and embOS/IP (TCP/IP stack). With the experience in programming efficiently on embedded systems, SEGGER created highly integrated, cost-



effective programming and development tools, such as the Flasher (stand-alone flash programmer) and the industry leading J-Link/J-Trace emulator.

SEGGER's intention is to cut software development time for embedded applications by offering affordable, high quality, flexible and easy-to-use tools and software components allowing developers to focus on their applications. Find out more at <http://www.segger.com>

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