SEGGER introduces streaming trace probe for SiFive RISC-V cores

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SEGGER's [J-Trace PRO](#) with [streaming trace](#), [Live Code Profiling,](#) and [Live Code Coverage](#) now supports all E-Series SiFive RISC-V cores with the BTM trace module.

[J-Trace PRO RISC-V](#), with its SuperSpeed USB 3.0 interface, enables continuous streaming trace via USB. This allows the processing of data from a target device in real time, giving users a deep insight into the application program. Streaming in real time allows for data capture over long periods of time with no limit set on the amount of trace data. This is ideal for code optimization and especially for finding infrequent, hard-to-reproduce bugs.

"Streaming trace is the ultimate key technology in code optimization," says Ivo Geilenbruegge, Managing Director of SEGGER. "Some of our most demanding customers, including SEGGER's own in-house engineers, use the J-Trace PRO for code optimization and verification. We are delighted that we can also make this experience available to developers working with SiFive RISC-V cores."

[J-Trace PRO](#) enables Live Code Profiling by detailing which instructions have been executed how often. This allows users to address runtime hotspots and to identify opportunities to optimize them. These profiles can later be exported using a tool like, for example, SEGGER's [Ozone](#) debugger for documentation and analysis. In addition, J-Trace PRO's Live Code Coverage lets engineers see code coverage at a glance, showing which instructions have or have not been executed. Code coverage is important for test verification. Hence, running the code coverage analysis in parallel with a test suite clearly shows whether all parts of the application are run through the test, which is a key element of functional tests.

J-Trace PRO is the pinnacle of the [SEGGER family of debug and trace probes](#). In addition to its signature streaming and real-time features, it also includes all the features of [J-Link](#), such as high-performance flashloaders, up to 4 [MB/s](#) download speed, and an [unlimited number of breakpoints](#) in the flash memory of MCUs.

For more information on J-Trace PRO RISC-V, please visit:

https://www.segger.com/products/debug-probes/j-trace/models/j-trace-pro-risc-v/

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About SEGGER

SEGGER Microcontroller GmbH has three decades of experience in Embedded Systems, producing cutting-edge RTOS and Software Libraries, J-Link and J-Trace debug and trace probes, a line of Flasher In-System Programmers and software development tools.

SEGGER's all-in-one solution emPower OS provides an RTOS plus a complete spectrum of software libraries including communication, security, data compression and storage, user interface software and more. Using emPower OS gives developers a head start, benefiting from decades of experience in the industry.

SEGGER's professional embedded development software and tools are simple in design, optimized for embedded systems, and support the entire embedded system development process through affordable, high-quality, flexible and easy-to-use tools.

The company was founded by Rolf Segger in 1992, is privately held, and is growing steadily. SEGGER also has a U.S. office in the Boston area and branch operations in Silicon Valley, Shanghai and the UK, plus distributors on most continents, making SEGGER's full product range available worldwide.

For more information on SEGGER, please visit www.segger.com.

Why SEGGER?

In short, SEGGER has a full set of tools for embedded systems, offers support through the entire development process, and has decades of experience as the Embedded Experts.

In addition, SEGGER software is not covered by an open-source or required-attribution license and can be integrated in any commercial or proprietary product, without the obligation to disclose the combined source.

Finally, SEGGER offers stability in an often volatile industry, making SEGGER a very reliable partner for long-term relationships.

For additional information please visit: www.segger.com

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