

# SEGGER adds 64-bit RISC-V support to Embedded Studio

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SEGGER's new release of <u>Embedded Studio for RISC-V</u> adds support for 64-bit RISC-V CPUs, including RV64I, RV64G/RV64E and RV64GC/RV64EC with floating-point unit.

It comes integrated with SEGGERs emRun C/C++ runtime and emFloat floating-point libraries, the SEGGER Linker, and the SEGGER Compiler, which together generate compact and highly efficient code. For maximum flexibility and compatibility, the GNU compiler and linker are also included.

Embedded Studio's built-in debugger natively supports J-Link and a full-fledged simulator, ideal for automated testing. With its integration of the GDB remote protocol, <u>Embedded Studio</u> enables the use of 3rd-party debug probes.

"64-bit embedded systems are gaining popularity with faster chips and bigger



applications," says Ivo Geilenbruegge, Managing Director of SEGGER. "As the Embedded Experts, we are always at the cutting edge of new technologies. As a long-standing member of the RISC-V foundation, we are committed to broad RISC-V support, from small 32-bit through to high-end 64-bit cores, from simple debug to flash programming to real-time trace. One IDE fits all."

As the debug probe of choice for Embedded Studio users, <u>J-Link</u> supports all standard 32- and 64-bit RISC-V cores, including non-intrusive background, system-bus memory access as well as <u>Real-Time Transfer</u> (RTT).

Features also available from within Embedded Studio include direct download into flash memory via an open flash loader interface, giving the RISC-V ecosystem access to SEGGER's vast catalog of <u>supported flash devices</u> and the opportunity to add flash programming for newly developed devices.

For additional information about RISC-V support from SEGGER visit: <a href="https://www.segger.com/risc-v/">https://www.segger.com/risc-v/</a>

For information about SEGGER Evaluation Software for RISC-V visit: <a href="https://www.segger.com/evaluate-our-software/risc-v/">https://www.segger.com/evaluate-our-software/risc-v/</a>

It simply works!



#### **About SEGGER**

SEGGER has over twenty-nine years of experience in Embedded Systems, producing cutting-edge <u>RTOS</u> and <u>Software Libraries</u>, J-Link and J-Trace <u>debug and trace probes</u>, a line of <u>Flasher In-System Programmers</u> and <u>software development tools</u>.

SEGGER's all-in-one solution emPower OS provides an RTOS plus a complete spectrum of software libraries including communication, security, data compression and storage, user interface software and more. Using emPower OS gives developers a head start, benefiting from decades of experience in the industry.

SEGGER's professional software and tools for Embedded System development are designed for simple usage and are optimized for the requirements imposed by resource-constrained embedded systems. The company also supports the entire development process with affordable, high-quality, flexible, easy-to-use tools.

The company was founded by Rolf Segger in 1992, is privately held, and is growing steadily. SEGGER also has a U.S. office in the Boston area and branch operations in Silicon Valley, Shanghai and the UK, plus distributors on most continents, making SEGGER's full product range available worldwide.

## Why SEGGER?

In short, SEGGER has a full set of tools for embedded systems, offers support through the entire development process, and has decades of experience as the Embedded Experts.

In addition, SEGGER software is not covered by an open-source or requiredattribution license and can be integrated in any commercial or proprietary product, without the obligation to disclose the combined source.

Finally, SEGGER offers stability in an often volatile industry making SEGGER a very reliable partner for long-term relationships.

For additional information please visit: www.segger.com

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