

SEGGER announces comprehensive support for SiFive Insight debug/trace platform

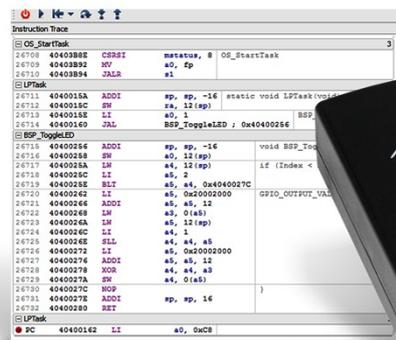
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SEGGER continues to strengthen its position in relation to the RISC-V instruction set architecture. The company's products are already fully compatible with SiFive's RISC-V processor cores, and now its J-Link probes deliver support for the new SiFive Insight debug/trace solution. This includes SiFive's latest Nexus-based trace implementation, which enables ongoing monitoring and recording of processor instruction execution.

Via SEGGER's highly popular J-Link PLUS, PRO and ULTRA debug probe options, plus the accompanying Ozone debugger and performance analysis software package, engineers are now able to take full advantage of SiFive Insight using on-chip trace functionality. Among the relevant features incorporated within the SEGGER debug probes is a backtracing capability (where the full execution history can be easily accessed and stepped through backwards). More advanced features, like code coverage and profiling, can also be employed - based on the execution counters processed by the J-Link software. The Ozone debug software package can subsequently generate detailed code coverage reports for software validation purposes.

"The continued support from SEGGER is a great asset to the RISC-V ecosystem, and the swift adoption of SiFive Insight is of great benefit to chip designers," says Drew Barbier, Director of Product Marketing, SiFive. "SEGGER has supported SiFive Core IP since 2017 and continues to be a valued partner in the expansion and adoption of RISC-V for embedded solutions. We look forward to continued co-operation as the RISC-V ecosystem continues to grow and evolve."

"SiFive continues to innovate with solid offerings for the RISC-V community," says Rolf Segger, founder of SEGGER. "We are proud to support their mission by offering high quality development tools and by ensuring their great new features can be fully leveraged using our industry leading J-Link debug probe and Ozone debugger."



For more information on SEGGER's support for RISC-V please visit:

<https://www.segger.com/risc-v/>

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About SEGGER

SEGGER Microcontroller has over twenty-five years of experience in Embedded Computer Systems, producing state-of-the-art software libraries, and offering a full set of hardware tools (for development and production) and software tools.

SEGGER provides an RTOS plus a complete spectrum of software libraries including communication, security, data compression and storage, user interface software and more. Using SEGGER software libraries gives developers a head start, benefiting from decades of experience in the industry.

SEGGER's professional software libraries and tools for Embedded System development are designed for simple usage and are optimized for the requirements imposed by resource-constrained embedded systems. The company also supports the entire development process with affordable, high-quality, flexible, easy-to-use tools.

The company was founded by Rolf Segger in 1992, is privately held, and is growing steadily. SEGGER also has a U.S. office in the Boston area and branch operations in Silicon Valley and the UK, plus distributors on most continents, making SEGGER's full product range available worldwide.

Why SEGGER?

In short, SEGGER has a full set of tools for embedded systems, offers support through the entire development process, and has decades of experience as the Embedded Experts.

In addition, SEGGER software is not covered by an open-source or required-attribution license and can be integrated in any commercial or proprietary product, without the obligation to disclose the combined source.

Finally, SEGGER offers stability in an often volatile industry making SEGGER a very reliable partner for long-term relationships.

For additional information please visit: www.segger.com

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