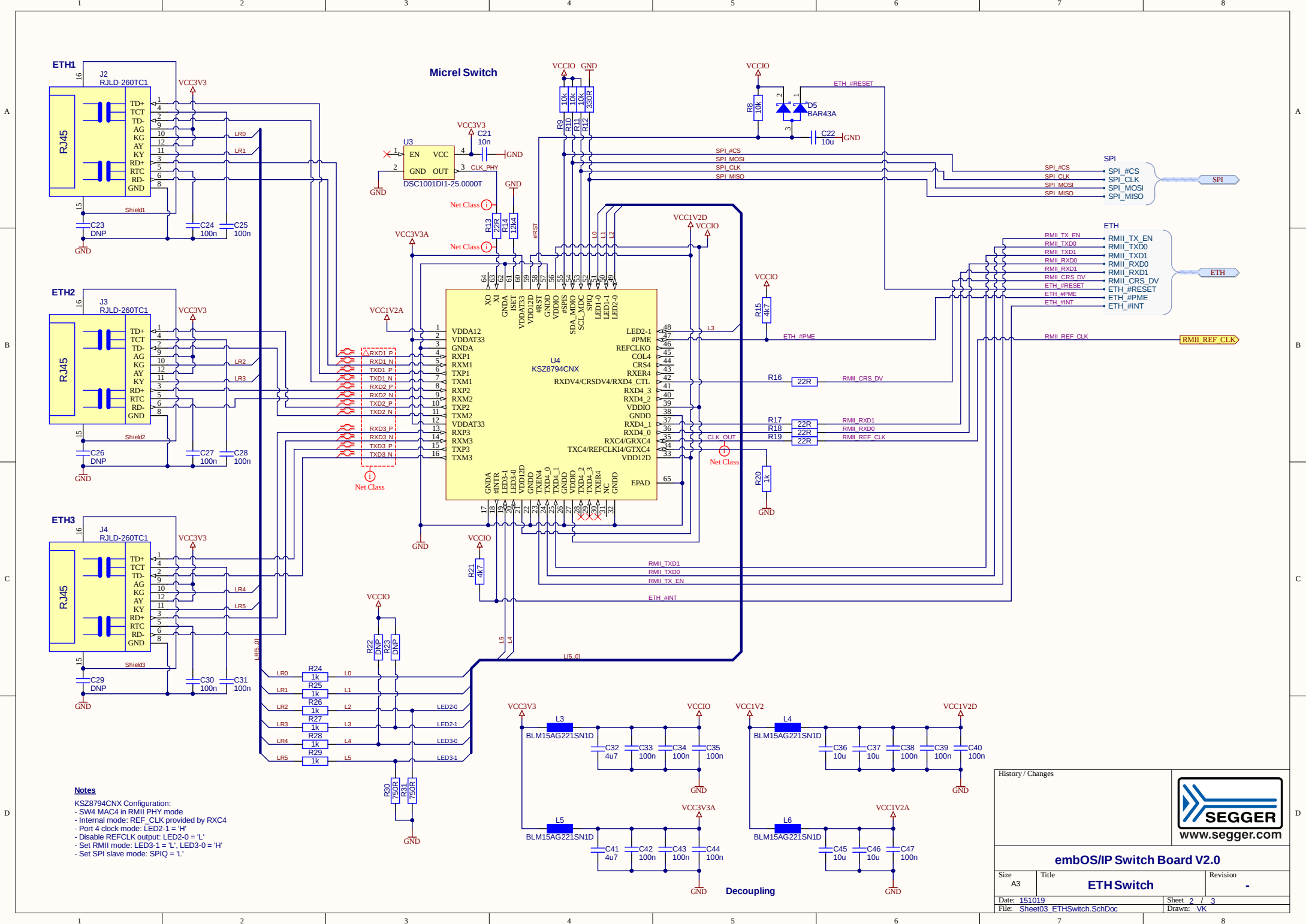


Configuration and status reading of the switch MACs and PHYs is done via SPI - and not the MDC/MDIO management interface of the controller

#NAME?

- Ceramic capacitors are of type X5R/X7R or C0G/NP0 with a voltage rating of at least 6.3V (100n at least 16V)
- Resistors are 1% tolerance types



History / Changes


www.segger.com

embOS/IP Switch Board V2.0

Size
A3

Title
ETH Switch

Revision
-

Date: 151019
File: Sheet03_ETHSwitch.SchDoc

Sheet 2 / 3
Drawn: VK

