

embOS- Classic-MPU

CPU & Compiler specifics
for the Linux Simulation

Document: UM01087
Software Version: 5.20.0.2
Revision: 0
Date: June 4, 2025



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Manual versions

This manual describes the current software version. If you find an error in the manual or a problem in the software, please inform us and we will try to assist you as soon as possible. Contact us for further information on topics or functions that are not yet documented.

Print date: June 4, 2025

Software	Revision	Date	By	Description
5.20.0.2	0	250604	MM	New software version.
5.20.0.1	0	250416	MM	New software version.
5.20.0.0	0	241118	TS	New software version.
5.18.3.0	0	240827	MM	Initial version.

About this document

Assumptions

This document assumes that you already have a solid knowledge of the following:

- The software tools used for building your application (assembler, linker, C compiler).
- The C programming language.
- The target processor.
- DOS command line.

How to use this manual

This manual explains all the functions and macros that the product offers. It assumes you have a working knowledge of the C language. Knowledge of assembly programming is not required.

Typographic conventions for syntax

This manual uses the following typographic conventions:

Style	Used for
Body	Body text.
Keyword	Text that you enter at the command prompt or that appears on the display (that is system functions, file- or pathnames).
Parameter	Parameters in API functions.
Sample	Sample code in program examples.
Sample comment	Comments in program examples.
Reference	Reference to chapters, sections, tables and figures or other documents.
GUI Element	Buttons, dialog boxes, menu names, menu commands.
Emphasis	Very important sections.

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Chapter 1

Using embOS

1.1 Installation

embOS is shipped as a zip-file in electronic form.

To install it, proceed as follows:

Extract the zip-file to any folder of your choice, preserving the directory structure of this file. Keep all files in their respective sub directories. Make sure the files are not read only after copying.

Assuming that you are using an IDE to develop your application, no further installation steps are required. You will find a prepared sample start project, which you should use and modify to write your application. So follow the instructions of section *First Steps* on page 11.

You should do this even if you do not intend to use the IDE for your application development to become familiar with embOS.

If you do not or do not want to work with the IDE, you should: Copy the library-file to your work-directory. The advantage is that when switching to an updated version of embOS later in a project, you do not affect older projects that use embOS, too. embOS does in no way rely on an IDE, it may be used without the IDE using scripts or build systems without any problem.

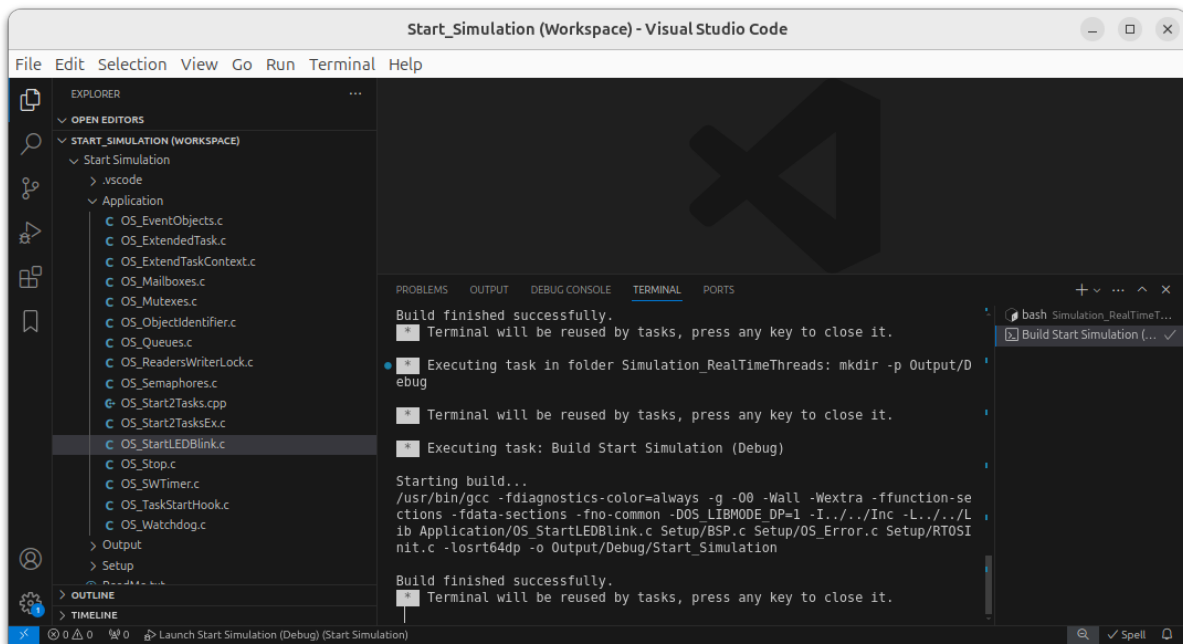
1.2 First Steps

After installation of embOS you can create your first multitasking application. You have received one ready to go sample start project and every other files needed in the subfolder `Start`. It is a good idea to use it as a starting point for all of your applications. The sample projects are contained in the subfolder `BoardSupport`.

To get your new application running, you should proceed as follows:

- Create a work directory for your application, for example `c:\work`.
- Copy the whole folder `Start` which is part of your embOS distribution into your work directory.
- Clear the read-only attribute of all files in the new `Start` folder.
- Open the sample project in one of the BSPs in `Start\BoardSupport` with your IDE (for example, by double clicking it).
- Build the project. It should be built without any error or warning messages.

After generating the project of your choice, the screen should look like this:



1.3 The example application OS_StartLEDBlink.c

The following is a printout of the example application OS_StartLEDBlink.c. It is a good starting point for your application. Note that the file actually shipped with your port of embOS may look slightly different from this one.

What happens is easy to see:

After initialization of embOS two tasks are created and started. The two tasks are activated and executed until they run into the delay, suspend for the specified time and continue execution.

```

/*****
 *                               SEGGER Microcontroller GmbH
 *                               The Embedded Experts
 *****/

----- END-OF-HEADER -----
File      : OS_StartLEDBlink.c
Purpose   : embOS sample program running two simple tasks, each toggling
            a LED of the target hardware (as configured in BSP.c).
*/

#include "RTOS.h"
#include "BSP.h"

static OS_STACKPTR int StackHP[128], StackLP[128]; // Task stacks
static OS_TASK      TCBHP, TCBLP;                 // Task control blocks

static void HPTask(void) {
    while (1) {
        BSP_ToggleLED(0);
        OS_TASK_Delay(50);
    }
}

static void LPTask(void) {
    while (1) {
        BSP_ToggleLED(1);
        OS_TASK_Delay(200);
    }
}

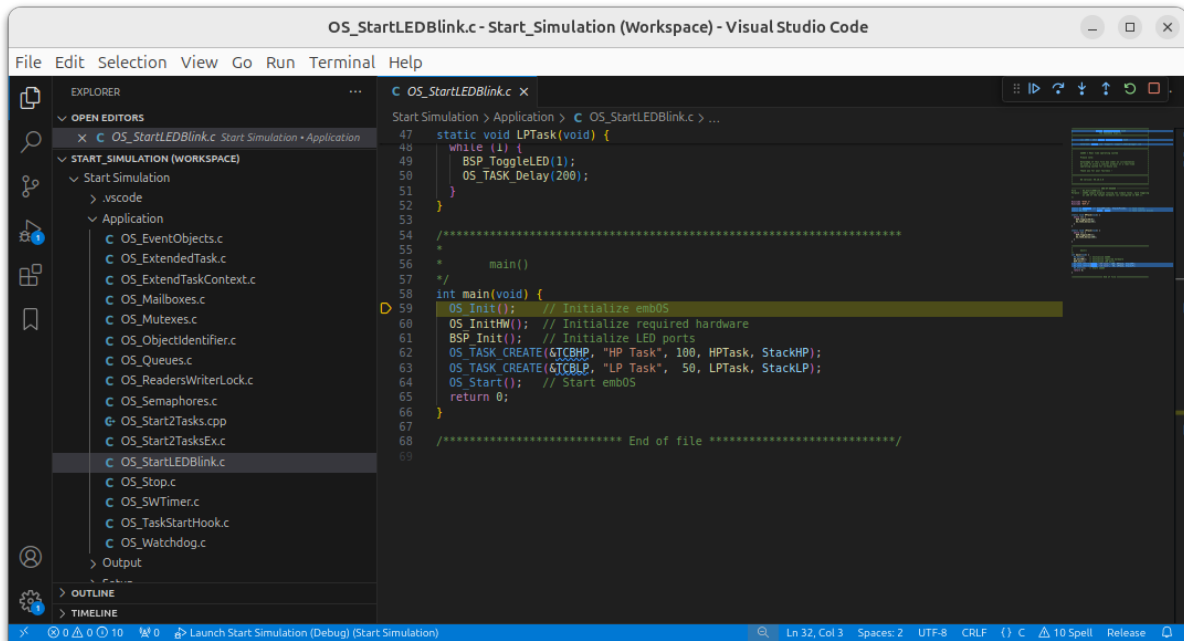
/*****
 *
 *      main()
 */
int main(void) {
    OS_Init();    // Initialize embOS
    OS_InitHW(); // Initialize required hardware
    BSP_Init();   // Initialize LED ports
    OS_TASK_CREATE(&TCBHP, "HP Task", 100, HPTask, StackHP);
    OS_TASK_CREATE(&TCBLP, "LP Task", 50, LPTask, StackLP);
    OS_Start();   // Start embOS
    return 0;
}

/***** End of file *****/

```

1.4 Stepping through the sample application

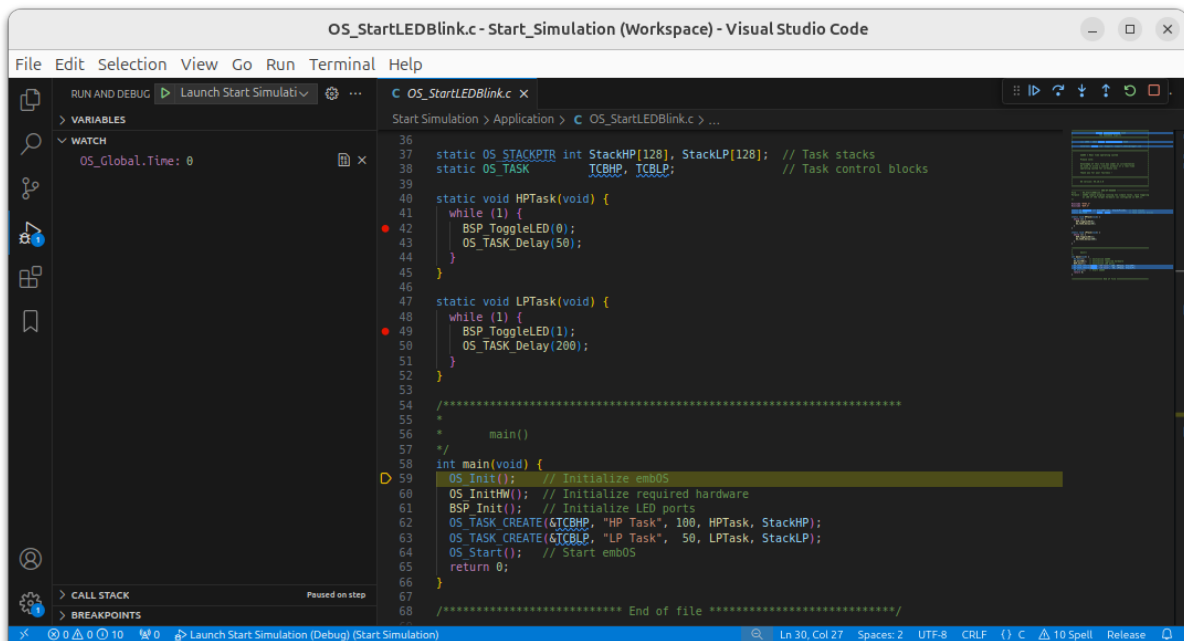
When starting the debugger, you will see the `main()` function (see example screen shot below). If the debugger does not halt at the `main()` function, set a breakpoint at the first instruction in the `main()` function.



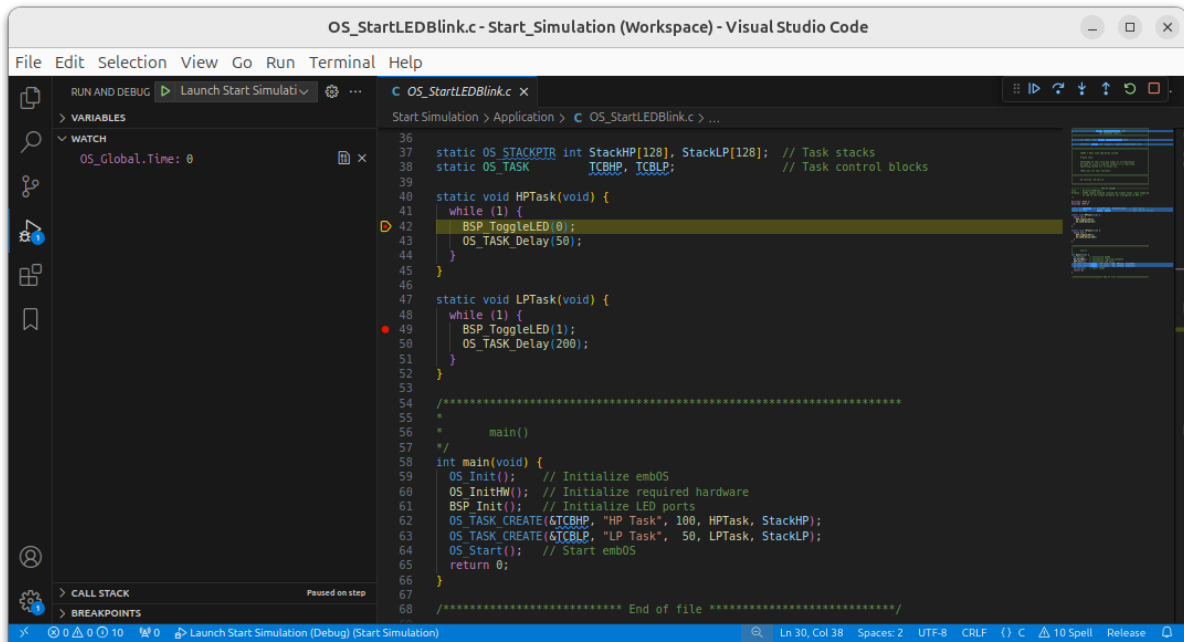
`OS_Init()` is part of the embOS library; you can therefore only step into it in disassembly mode. It initializes the relevant OS variables.

`OS_InitHW()` is part of `RTOSInit.c` and therefore part of your application. Its primary purpose is to initialize the hardware required to generate the system tick interrupt for embOS. Step through it to see what is done.

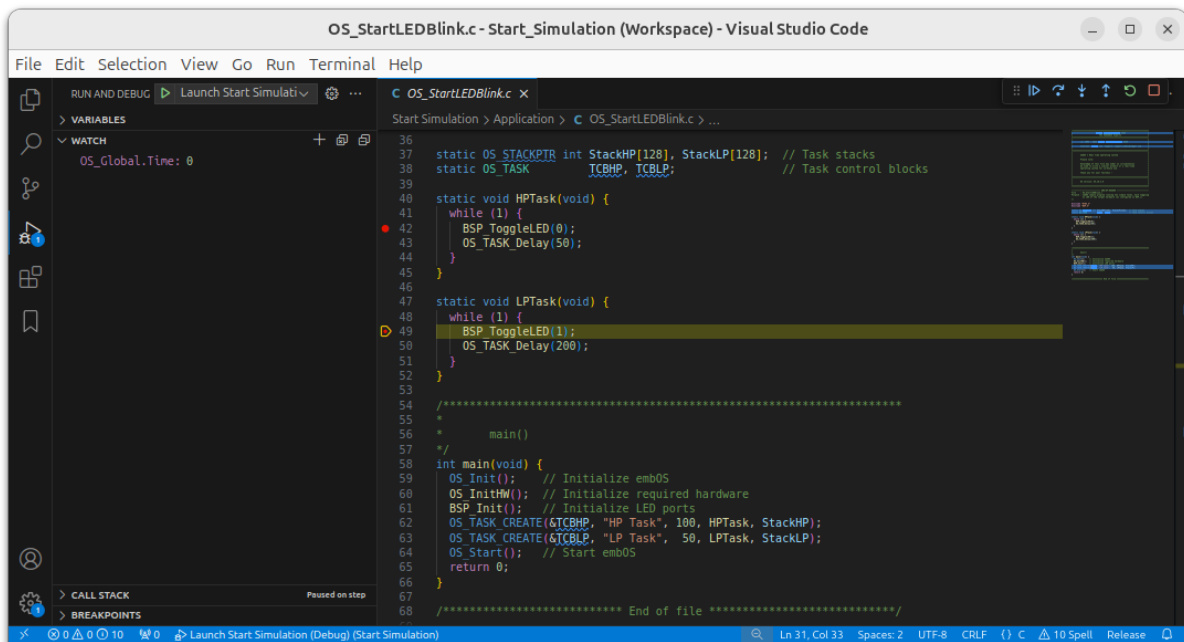
`OS_Start()` should be the last line in `main()`, because it starts multitasking and does not return, unless `OS_Stop()` is used. Before you step into `OS_Start()`, you should set two breakpoints in the two tasks as shown below.



Step over `OS_Start()`, or step into `OS_Start()` in disassembly mode until you reach the highest priority task. If you continue stepping, the first LED of your device will be switched on, the `HPTask()` will run into its delay and therefore, embOS will start the task with the lower priority.



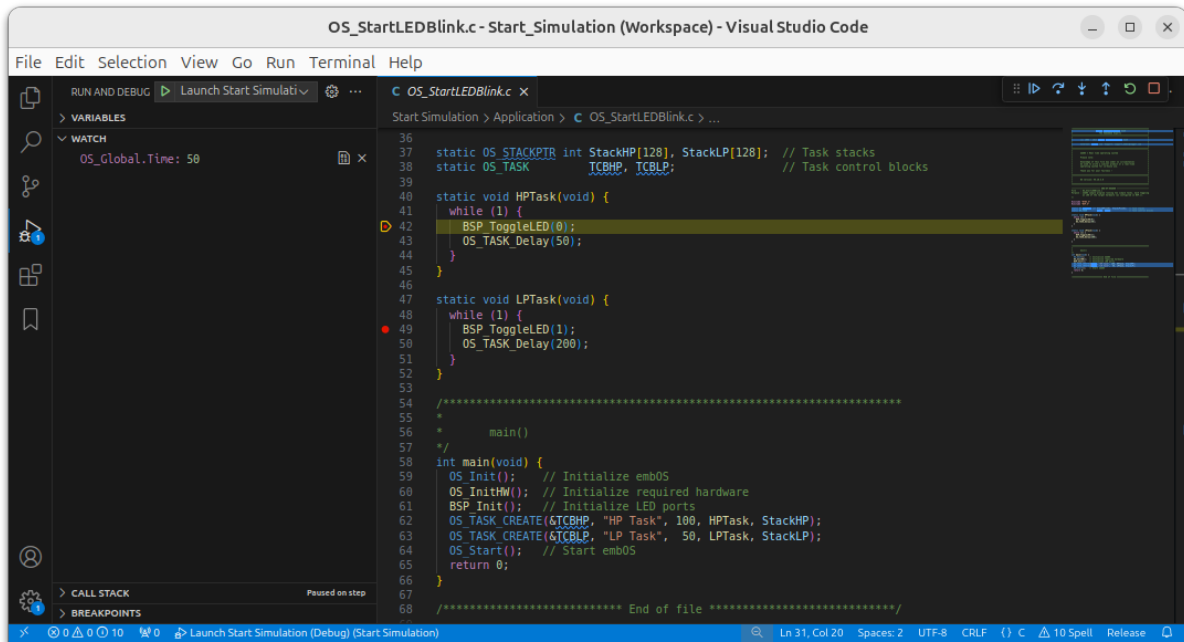
Continuing to step through the program, the `LPTask()` will switch on the other LED and then run into its delay.



As there is no other task ready for execution when `LPTask()` runs into its delay, embOS will suspend `LPTask()` and switch to the idle process, which is always executed if there is nothing else to do (no task is ready, no interrupt routine or timer executing).

The embOS simulation does not contain an `OS_Idle()` function which is implemented in normal embOS ports.

When you step over the `OS_TASK_Delay()` function of the `LPTask()`, you will arrive back in the `HPTask()`. As can be seen by the value of `embOS` timer variable `OS_Global.Time`, shown in the Watch window, `HPTask()` continues operation after expiration of the 50 system tick delay.



Please note, that delays seem to be longer than expected. When the debugger stops at a breakpoint, it takes some time until the screen is updated and the `OS_Global.Time` variable is examined. Therefore `OS_Global.Time` may show larger values than expected.

You may now disable the two breakpoints in our tasks and continue the execution of the application to see how the simulated device runs in real time.

Chapter 2

Build your own application

2.1 Introduction

This chapter provides all information to set up your own embOS simulation project. To build your own application, you should always start with the supplied sample project. Therefore, select the embOS sample project as described in chapter *First Steps* on page 11 and modify the project to fit your needs. Using an embOS start project as starting point has the advantage that all necessary files are included and all settings for the project are already done.

2.2 Required files for an embOS

To build an application using the embOS simulation, the following files from your embOS distribution are required and have to be included in your project:

File	Usage
Start\Lib\	
lib*.a	One of the embOS libraries.
Start\Inc\	
RTOS.h	Declares all embOS API functions and data types and has to be included in any source file using embOS functions.
Start\BoardSupport\Simulation\Setup\	
RTOSInit.c	Contains initialization code for the embOS timer interrupt handling and simulation.
OS_Error.c	Contains the OS_Error() function that is called when an application error occurs.

2.3 Select a start project configuration

The embOS simulation comes with a start project which includes the following configurations:

Configuration	Description
Debug, 32-bit	32-Bit configuration that can be used for development and debugging.
Release, 32-bit	32-Bit configuration used to build a release executable. It may be used for demonstration purposes.

2.4 Add your own code

For your own code, you may add a new folder to the project or add additional files to the `Application` folder. You may modify or replace the sample application source file in the `Application` directory.

The `main()` function has to be used as an entry point of the embOS simulation. Your `main()` function has to initialize embOS by calling `OS_Init()` and `OS_InitHW()` prior to any other embOS functions that are called.

2.5 Rebuilding the embOS libraries

New libraries for the embOS simulation can only be built using the source version of the embOS simulation.

- Modify the `Prep.sh` bash script in the root directory of the embOS simulation source distribution to set the path to the compiler toolchain.
- Finally start `M.sh` to produce a new `Start\Lib\` folder which then contains the new libraries.

Chapter 3

Libraries

3.1 Naming conventions for prebuilt libraries

embOS is shipped with different pre-built libraries with different combinations of features.

The libraries are named as follows:

`libos<Architecture><LibMode>.a`

Parameter	Meaning	Values
Architecture	Specifies the architecture	32 : 32-bit embOS (x86)
LibMode	Specifies the library mode	xr : Extreme Release r : Release s : Stack check sp : Stack check + profiling d : Debug dp : Debug + profiling + Stack check dt : Debug + profiling + Stack check + trace

Example

`libos32dp.lib` is the library for the 32-bit embOS simulation with debug and profiling support.

`libos32r.lib` is the library for the 32-bit embOS simulation and the embOS release build.

Chapter 4

CPU and compiler specifics

4.1 Interrupt and thread safety

Using embOS with specific calls to standard library functions (e.g. heap management functions) requires thread-safe system libraries if these functions are called from several tasks or interrupts. The thread safety provided by GNU C library is not enough to ensure thread safety for the embOS simulation. Therefore, system library functions that need to be thread-safe and that are called by the simulation need to be overwritten and made thread-safe.

The Setup directory in the embOS BSP contains the file `OS_ThreadSafe.c` which overwrites heap management and some other functions. This is done by providing own definitions for the system library functions used by the simulation. These definitions then ensure thread safety before calling the actual library function whose address was previously loaded from the shared library. System library functions that are not yet overwritten in `OS_ThreadSafe.c` must be made thread-safe in the same way as it is done for the other functions in `OS_ThreadSafe.c`.

Overwriting functions by providing custom definitions works only as long as the identifiers of the system library functions do not change. Should the identifier of a function change, e.g. due to optimization, then the function with the changed identifier is called instead of the thread-safe custom function implementation. The include header files of the GNU C library use the `_FORTIFY_SOURCE` macro to control code hardening, which also results in function inlining. Inlining the calls to system library functions can result in the call of different function identifiers as expected which again results in the thread-safe custom function implementations `OS_ThreadSafe.c` being omitted. Therefore, `_FORTIFY_SOURCE` must be defined to 0 when the compiler is called.

```
gcc -U_FORTIFY_SOURCE -D_FORTIFY_SOURCE=0 ...
```

With GNU C library versions prior to 2.34 it is not possible to overwrite heap management functions and retrieve their address from the shared library. In this case malloc hooks are used which were removed with version 2.34. `OS_ThreadSafe.c` automatically checks the GNU C library version and uses the appropriate implementation.

Chapter 5

Stacks

5.1 Task stacks

Every embOS task has to have its own stack. Task stacks can be located in any RAM memory location. In embOS simulation, every task runs as a separate thread. The real “task” stack is managed by Linux. Declaration and size of task stacks in your application are necessary for generic embOS functions, but do not affect the stack size of the generated Linux thread. A stack check and stack overflows are not simulated.

5.2 System stack

The system stack used during startup is managed by Linux. A stack check and stack overflows are not simulated.

5.3 Interrupt stack

Simulated interrupts in the embOS simulation run as Linux thread. ISR stacks are managed by Linux. Since every ISR has its own stack, embOS cannot simulate interrupt stack check and stack overflows.

Chapter 6

Interrupts

6.1 Introduction

With the embOS simulation, interrupts have to be simulated and thus differ from those used in embedded applications. The following chapter describes the simulation and handling of interrupts in the embOS simulation.

6.2 How interrupt simulation works

All interrupt service routines (ISR) are executed by individual threads created by the embOS simulation API function `OS_SIM_CreateISRThread()`. `OS_SIM_CreateISRThread()` expects two functions: The actual ISR and a controller function. Both functions are executed in a loop, beginning with a call to the controller function. The controller function has to block the thread until the interrupt is supposed to occur. To do so, it can call any blocking Linux function to simulate the behavior of the hardware interrupt. If the controller function returns with a value other than 0, the ISR is executed as soon as interrupts enabled. When the ISR finishes execution, the controller function is called again. This is repeated until the ISR thread is terminated by a call to `OS_SIM_DeleteISRThread()` or the ISR thread terminates itself by returning 0 from the controller function.

Note

To ensure that ISRs are not executed by accident, the return value of Linux functions called by the controller function should be checked.

The function `OS_SIM_DeleteISRThread()` terminates threads by detaching them and sending an asynchronous cancellation request to the ISR thread. It is not guaranteed that threads, that received a cancellation request, terminate immediately. Cancellation may for instance be delayed if the ISR thread was suspended due to a blocking Linux call. For example, if the controller function receives the cancellation request while being suspended due to a call to `pthread_cond_wait()`, the condition must be signaled and the thread must be able to lock the mutex. Only then the thread will be terminated. Thus, it's the responsibility of the user to ensure that the cancellation succeeds when the thread is canceled while executing the controller function.

6.3 Creating interrupts

The following example shows how to create a simple timer interrupt. `_TimerHandler()` is the ISR function and `_TimerController()` is the controller function. To control when the timer interrupt is supposed to occur the Linux function `usleep()` declared in `unistd.h` is used.

```
static void _TimerHandler(void) {
    OS_INT_Enter();    // Tell embOS that interrupt code is running
    DoTimerHandling(); // Any functionality can be added here
    OS_INT_Leave();     // Tell embOS that interrupt code ends
}

static int _TimerController(void) {
    usleep(1000);
    return 1;
}
```

To create the ISR thread, the following call needs to be done.

```
void* TimerISRHandle;
TimerISRHandle = OS_SIM_CreateISRThread(_TimerHandler, _TimerController);
```

The returned handle, that is stored in `TimerISRHandle`, can then be used to delete the ISR thread.

```
OS_SIM_DeleteISRThread(TimerISRHandle);
```

6.4 Interrupt priorities

All interrupts have the same priority and nesting of interrupts is not supported. ISR threads have the same priority as all other threads and the scheduling of them is up to the Linux scheduler.

6.5 API functions

Routine	Description	main	Task	ISR	Timer
OS_SIM_CreateISRThread()	Created an ISR thread.	•	•	•	•
OS_SIM_CreateISRThreadEx()	Created an ISR thread and sets its name.	•	•	•	•
OS_SIM_DeleteISRThread()	Deletes an ISR thread.	•	•		

6.5.1 OS_SIM_CreateISRThread()

Description

OS_SIM_CreateISRThread() creates an ISR thread that executes the ISR whenever the controller function returns with a value other than 0.

Prototype

```
void* OS_SIM_CreateISRThread(OS_ISR_HANDLER*   pfISR,  
                             OS_ISR_CONTROLLER* pfController);
```

```
typedef void OS_ISR_HANDLER(void);  
typedef int OS_ISR_CONTROLLER(void);
```

Parameters

Parameter	Description
<code>pfISR</code>	Pointer to void function that serves as the ISR.
<code>pfController</code>	Pointer to void function that controls when the ISR is supposed to be executed.

Return Value

A handle to the created ISR thread which can be used with OS_SIM_DeleteISRThread().

Additional Information

If the controller function returns 0, the ISR thread is terminated. If it returns a value other than zero, the ISR function is executed as soon as interrupts are enabled and then the controller function is called again.

6.5.2 OS_SIM_CreateISRThreadEx()

Description

OS_SIM_CreateISRThreadEx() creates an ISR thread that executes the ISR whenever the controller function returns with a value other than 0 and sets the name of the created pthread.

Prototype

```
void* OS_SIM_CreateISRThreadEx(      OS_ISR_HANDLER*   pfISR,  
                                   OS_ISR_CONTROLLER* pfController,  
                                   const char*         sThreadName);
```

```
typedef void OS_ISR_HANDLER(void);  
typedef int OS_ISR_CONTROLLER(void);
```

Parameters

Parameter	Description
<code>pfISR</code>	Pointer to void function that serves as the ISR.
<code>pfController</code>	Pointer to void function that controls when the ISR is supposed to be executed.
<code>sThreadName</code>	The name of the pthread.

Return Value

A handle to the created ISR thread which can be used with OS_SIM_DeleteISRThread().

Additional Information

If the controller function returns 0, the ISR thread is terminated. If it returns a value other than zero, the ISR function is executed as soon as interrupts are enabled and then the controller function is called again.

6.5.3 OS_SIM_DeleteISRThread()

Description

`OS_SIM_DeleteISRThread()` deletes an ISR thread by sending a cancellation request to the thread.

Prototype

```
void OS_SIM_DeleteISRThread(void* pThreadHandle);
```

Parameters

Parameter	Description
<code>ThreadHandle</code>	Handle of the ISR thread to delete.

Additional Information

The ISR thread is detached and will be terminated asynchronously by sending a cancellation request to the thread. It is not guaranteed that threads, that received a cancellation request, terminate immediately. Cancellation may for instance be delayed if the ISR thread was suspended due to a blocking Linux call. For example, if the controller function receives the cancellation request while being suspended due to a call to `pthread_cond_wait()`, the condition must be signaled and the thread must be able to lock the mutex. Only then the thread will be terminated. Thus, it's the responsibility of the user to ensure that the cancellation succeeds when the thread is canceled while executing the controller function.

Chapter 7

MPU support

7.1 embOS-Classic-MPU Cortex-M support

embOS-Classic-MPU Sim Linux can compile the code of applications that are written for embOS-Classic-MPU and Cortex-M architectures. It provides the same port-specific embOS-Classic-MPU API functions, structures and macros so that no modifications to the code is required. The simulation also performs all non-hardware related debug checks and assertions to locate incorrect use of the embOS-Classic-MPU API. However, the behavior of the Cortex-M MPU hardware is not simulated.

Chapter 8

Calling blocking non-embOS functions from tasks

8.1 Introduction

The embOS simulation is typically used to simulate real embedded applications. This may require the usage of potentially blocking non-embOS functions from tasks. Calling blocking embOS functions will suspend the task for the time it is waiting and allows tasks with lower priority to be scheduled by embOS. Calling blocking non-embOS functions will freeze the calling task and no other task with lower priority will be scheduled. This may cause the whole simulation to stop until the blocking task continues execution. To avoid this, two embOS API functions are available to manage the call of blocking non-embOS functions.

Similar to handling critical regions, there is one entry function (`OS_SIM_EnterSysCall()`), which has to be called before the blocking non-embOS function, and one exit function (`OS_SIM_LeaveSysCall()`), which has to be called after the blocking non-embOS function.

The Application folder of the embOS shipment contains the sample application `OS_Sim-Blocked.c`, which demonstrates these functions' usage on blocking non-embOS function calls.

8.2 API functions

Routine	Description	main	Task	ISR	Timer
<code>OS_SIM_EnterSysCall()</code>	Must be called prior to calling any blocking non-embOS function from a task.		•		
<code>OS_SIM_LeaveSysCall()</code>	Must be called after calling any blocking non-embOS API function from a task, and before any other embOS API function is called.		•		

8.2.1 OS_SIM_EnterSysCall()

Description

OS_SIM_EnterSysCall() has to be called before a blocking non-embOS function is called from a task.

Prototype

```
void OS_SIM_EnterSysCall(void);
```

Additional information

After calling OS_SIM_EnterSysCall(), no further embOS API function except OS_SIM_LeaveSysCall() must be called.

Example

```
...
OS_SIM_EnterSysCall();
// Any blocking non-embOS function may be called now.
...
recv (socket, pBuf, len, flags);
// Any other code may follow.
// No embOS function must be called except OS_SIM_LeaveSysCall().
...
OS_SIM_LeaveSysCall();
// From now on, calling other embOS functions is allowed.
...
```

8.2.2 OS_SIM_LeaveSysCall()

Description

OS_SIM_LeaveSysCall() has to be called after execution of a blocking non-embOS function, before any other embOS function is called.

Prototype

```
void OS_SIM_LeaveSysCall(void);
```

Additional information

It must be called only when OS_SIM_LeaveSysCall() has been called by the same task before.

Example

```
...
OS_SIM_EnterSysCall();
// Any blocking non-embOS function may be called now.
...
recv (socket, pBuf, len, flags);
// Any other code may follow.
// No embOS function must be called except OS_SIM_LeaveSysCall().
...
OS_SIM_LeaveSysCall();
// From now on, calling other embOS functions is allowed.
...
```

Chapter 9

RTT and SystemView

9.1 SEGGER Real Time Transfer

With SEGGER's Real Time Transfer (RTT) it is possible to output information from the target microcontroller as well as sending input to the application at a very high speed without affecting the target's real time behavior. SEGGER RTT can be used with any J-Link model and any supported target processor which allows background memory access.

RTT is included with many embOS start projects. These projects are by default configured to use RTT for debug output. Some IDEs, such as SEGGER Embedded Studio, support RTT and display RTT output directly within the IDE. In case the used IDE does not support RTT, SEGGER's J-Link RTT Viewer, J-Link RTT Client, and J-Link RTT Logger may be used instead to visualize your application's debug output.

For more information on SEGGER Real Time Transfer, refer to [segger.com/jlink-rtt](https://www.segger.com/jlink-rtt).

9.2 SEGGER SystemView

SEGGER SystemView is a real-time recording and visualization tool to gain a deep understanding of the runtime behavior of an application, going far beyond what debuggers are offering. The SystemView module collects and formats the monitor data and passes it to RTT.

SystemView is included with many embOS start projects. These projects are by default configured to use SystemView in debug builds. The associated PC visualization application, SystemView, is not shipped with embOS. Instead, the most recent version of that application is available for download from our website.

SystemView is initialized by calling `SEGGER_SYSVIEW_Conf()` on the target microcontroller. This call is performed within `OS_InitHW()` of the respective `RTOSInit*.c` file. As soon as this function was called, the connection of the SystemView desktop application to the target can be started. In order to remove SystemView from the target application, remove the `SEGGER_SYSVIEW_Conf()` call, the `SEGGER_SYSVIEW.h` include directive as well as any other reference to `SEGGER_SYSVIEW_*` like `SEGGER_SYSVIEW_TickCnt`.

For more information on SEGGER SystemView and the download of the SystemView desktop application, refer to [segger.com/systemview](https://www.segger.com/systemview).

Note

SystemView uses embOS timing API to get at start the current system time. This requires that `OS_TIME_ConfigSysTimer()` was called before `SEGGER_SYSVIEW_Start()` is called or the SystemView PC application is started.

Chapter 10

Technical data

10.1 Resource Usage

The memory requirements of embOS for RAM differs depending on the used features, CPU, compiler, and library model. The following values are measured using embOS library mode `OS_LIBMODE_XR`.

Module	Memory type	Memory requirements
embOS kernel	RAM	156 bytes
Task control block	RAM	336 bytes
Software timer	RAM	20 bytes
Task event	RAM	0 bytes
Event object	RAM	16 bytes
Mutex	RAM	16 bytes
Semaphore	RAM	8 bytes
RWLock	RAM	28 bytes
Mailbox	RAM	24 bytes
Queue	RAM	32 bytes
Watchdog	RAM	12 bytes
Fixed Block Size Memory Pool	RAM	32 bytes